

HONEY KUMAR

Assistant Professor Hindustan College of Science and Technology-MATHURA-281122

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Qualification	:	M.Tech (Engineering Systems)
Department	:	Electronics and Communication Engineering
Experience (Academics/Industry/Research)	:	8+ Years
Research Interest	:	VLSI Design, Soft computing
PhD Supervising	:	NA
BTech/MTech/MPhil		BTech: 14
Dissertation supervised		MTech :04
Sponsored Research Project	:	NA
/ Consultancy		
Research Publications	:	Journal : 01
		Conference : 01

Conference:

• ANUJ AGRAWAL, HONEY KUMAR "POWER REDUCTION THROUGH DIFFERENT LOW POWER CMOS TECHNIQUE IN POWER GATING SWITCH" 2ND INTERNATIONAL CONFERENCE ON NEW FRONTIERS OF ENGINEERING, SCIENCE, MANAGEMENT AND HUMANITIES (ICMFESMH-2017), PROC IN IJEECS, ISSN 2348-117X; VOLUME 6, ISSUE 6; PP 111–116 JUNE 2017.

Certifications:

- Completed the NPTEL course "The Joy of Computing using Python" in 2021.
- Completed the NPTEL course "CMOS Digital VLSI Design" in 2019.
- Completed the MOOC course on "Python Programming A Practical Approach" in 2018.

Patents	A patent on "Secret key management method for distributed wireless
	sensor network".

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